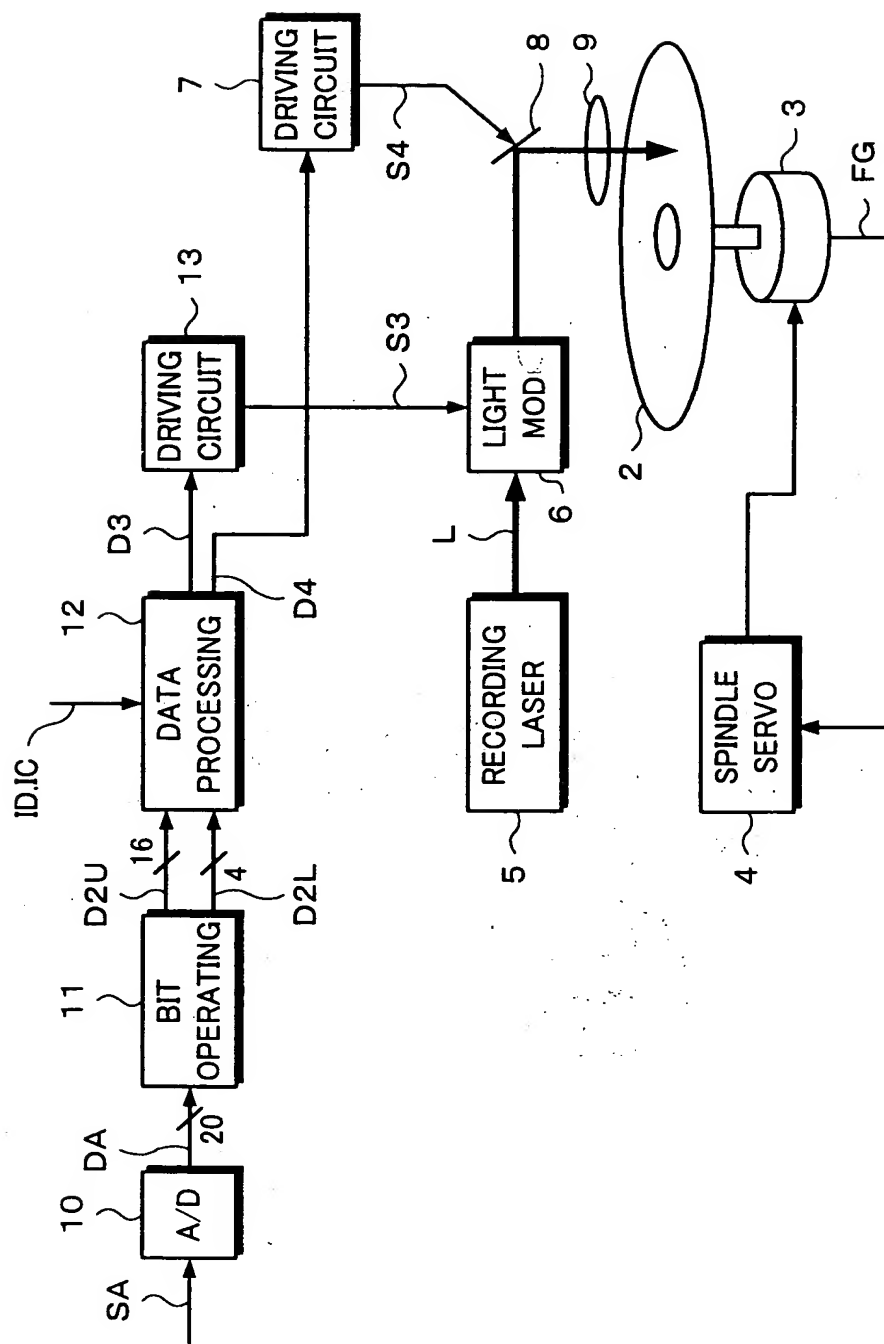


Fig. 1



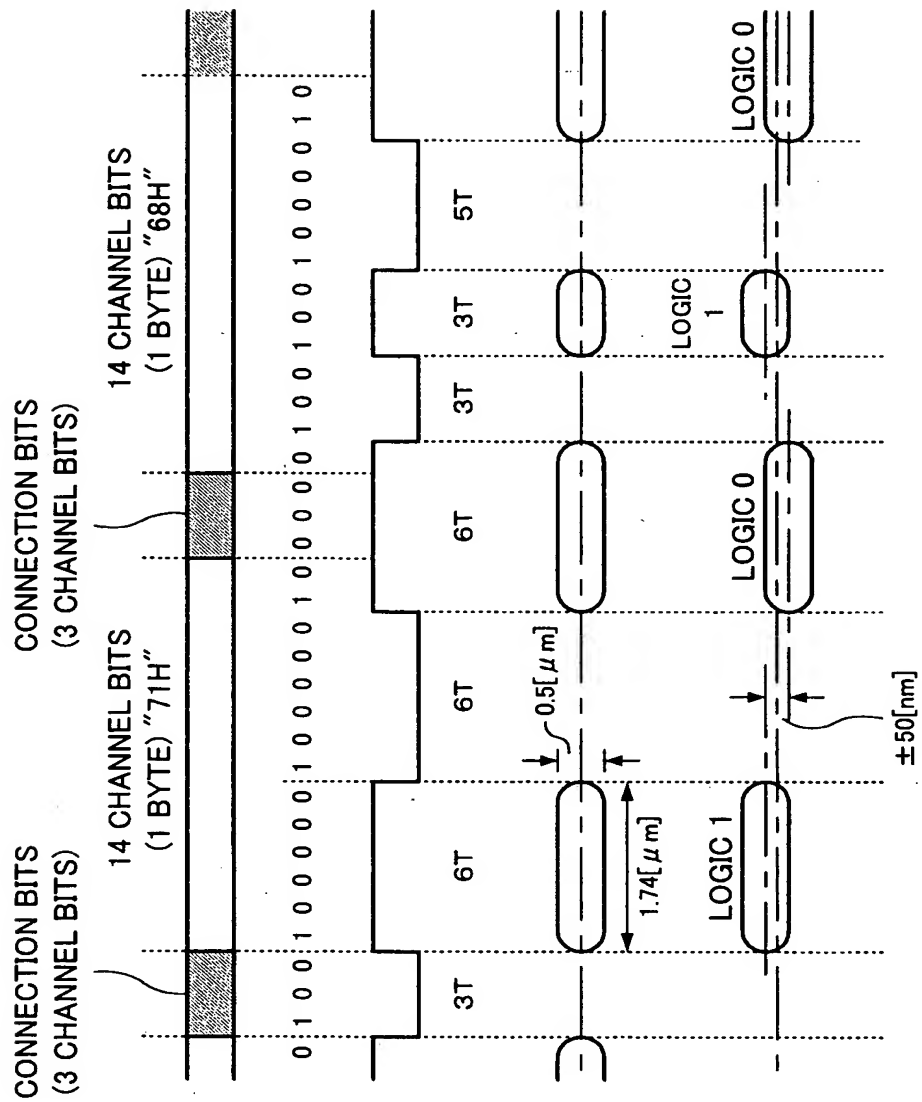


Fig. 2A

Fig. 2B

Fig. 2C

Fig. 2D

Fig. 3

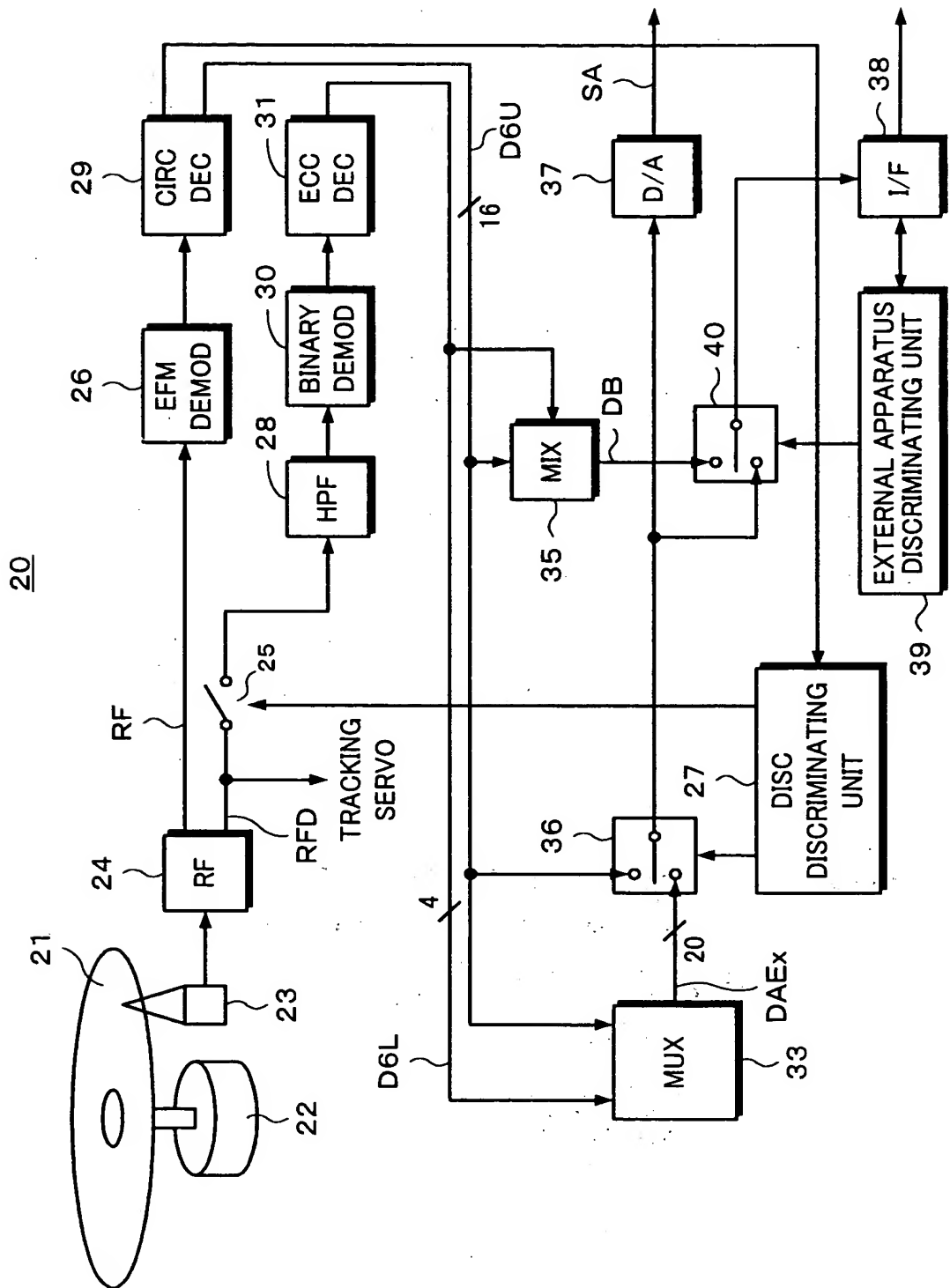


Fig. 4

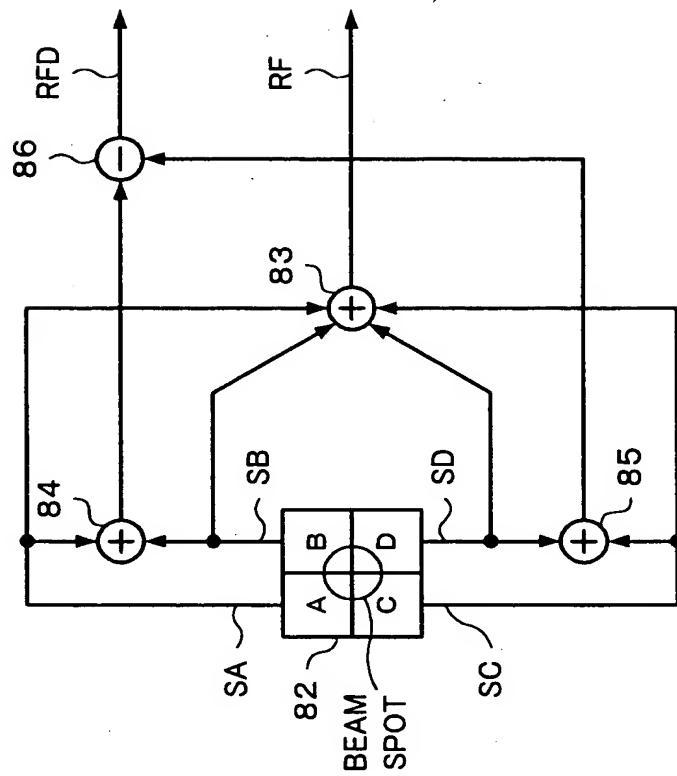


Fig. 5

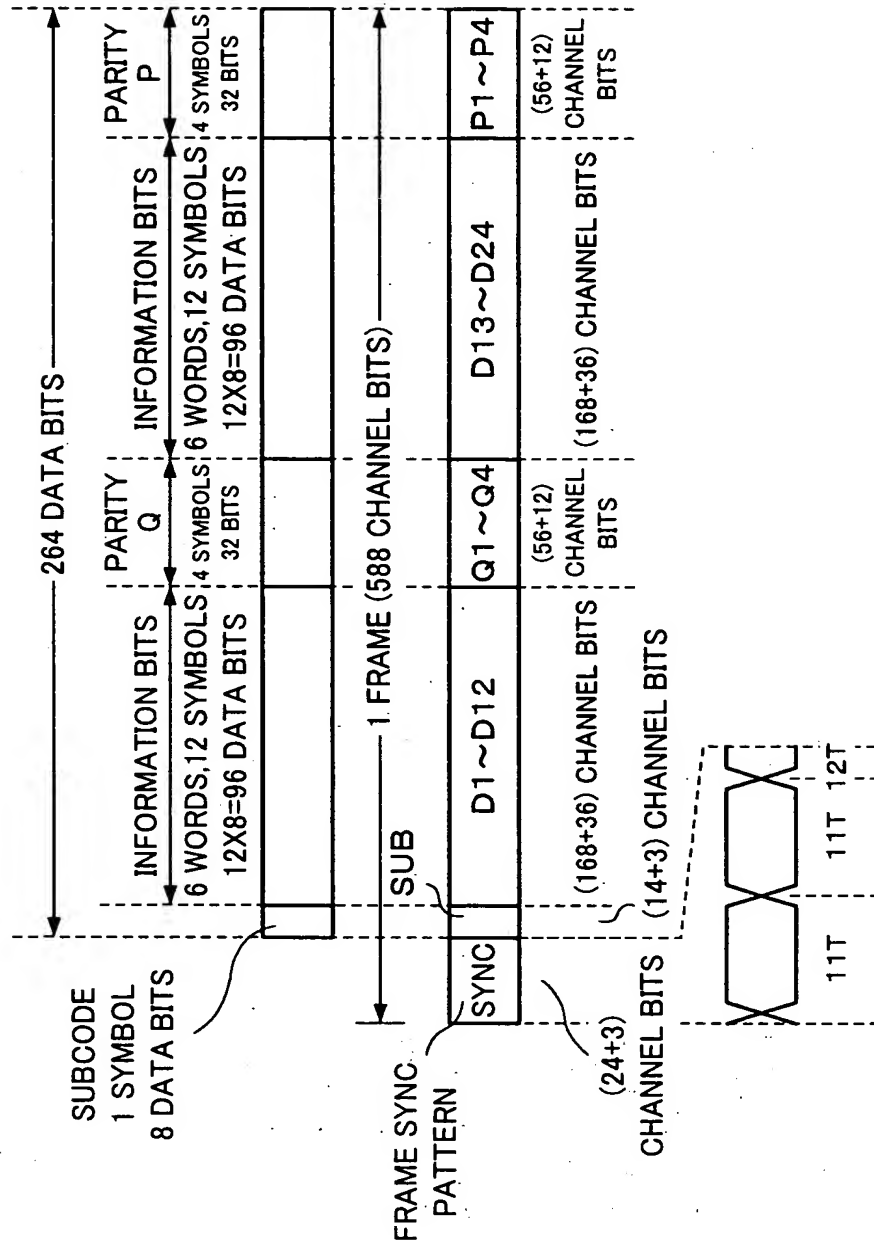


Fig. 6

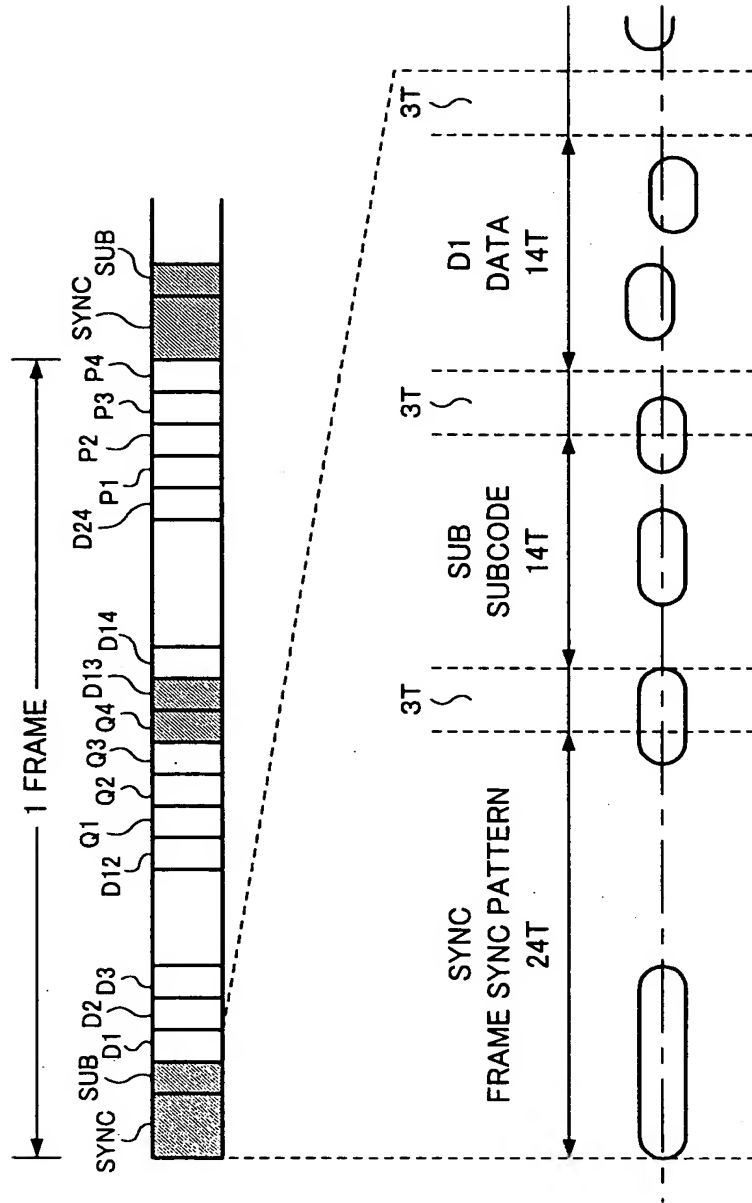


Fig. 7A

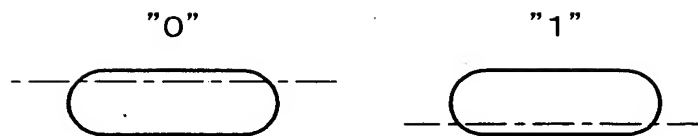


Fig. 7B

DATA WORD (4 BITS)	CODE WORD (5 BITS)
0000	00101
0001	00110
0010	01001
0011	01010
0100	01011
0101	01100
0110	01101
0111	01110
1000	10001
1001	10010
1010	10011
1011	10100
1100	10101
1101	10110
1110	11001
1111	11010

Fig. 8

